EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

E5-01035-SA (4)

PUBLICATION NUMBER : 11075109 PUBLICATION DATE : 16-03-99

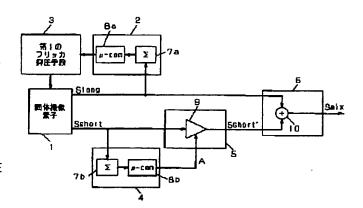
APPLICATION DATE : 01-04-98 APPLICATION NUMBER : 10088470

APPLICANT: MATSUSHITA ELECTRIC IND CO LTD;

INVENTOR: SANO TOSHIYUKI;

INT.CL. : H04N 5/235 G03B 7/08 H04N 5/335

TITLE : SOLID STATE IMAGE PICKUP DEVICE



ABSTRACT :

PROBLEM TO BE SOLVED: To provide a solid state image pickup device which can suppress the flicker at both high and low luminance parts by detecting and suppressing the flicker components for each of signals of different exposure value and then performing a synthesizing process.

SOLUTION: A short time exposure signal Sshort outputted from a solid state image pickup device 1 is inputted to a 2nd flicker detection means 4. When the means 4 detects the flicker, a 2nd flicker suppression means 5 inputs the gain A outputted from the means 4 to an amplifier circuit 9 to correct the gain A for the signal Sshort and outputs a signal Sshort having suppressed flicker. An image synthesizing means 6 inputs a long time exposure signal Slong having suppressed flicker and inputs the signal Sshort to an adder circuit 10 to add both signals together and outputs a synthetic image signal Smix. Thus, the flicker can be suppressed at both high and low luminance parts while a dynamic range is extended.

COPYRIGHT: (C)1999,JPO